

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 09/452,581
Applicant: Fred D. Bailey et al.
Filed: 12/02/1999
TC/AU.: 2815
Examiner: Jesse A. Fenty

Confirmation No. 1638

Docket No.: TI-27935
Customer No.: 23494

Title: AN INTEGRATED CIRCUIT HAVING A THIN FILM RESISTOR LOCATED
WITHIN A MULTILEVEL DIELECTRIC BETWEEN AN UPPER AND LOWER
METAL INTERCONNECT LAYER

REQUEST FOR EXTENSION OF TIME

Commissioner of Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by
facsimile to the U.S. Patent and Trademark Office at 703-672-9318 on
the date shown below.


Jacqueline J. Garner, Reg. No. 36,144November 3, 2003
Date

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

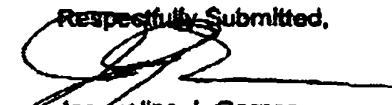
The fee for this extension is indicated below:

One Month (\$110)
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 Four Months (\$1,480)

Please charge the fee to deposit account no. 20-0688. Any further necessary extension of time is hereby requested. Charge any and all fees to deposit account no. 20-0688. An original and two copies of this sheet are enclosed.

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Respectfully Submitted,


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